

DEC 23 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Clynes, et al. Docket No: TI-32423  
Serial No: 09/862,523 Examiner: Moe, Aung Soe  
Filed: 5/22/2001 Art Unit: 2685  
For: ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING FOR CMOS IMAGERS

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER**  
**TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION I hereby  
certify that the following papers are being transmitted by  
facsimile to the U.S. Patent and Trademark Office at 571-  
273-8300 on the date shown below.

*Tommie Chambers*  
Tommie Chambers 12-23-05

Applicant hereby appeals to the Board of Appeals from the decision mailed September 9, 2005, of the  
Primary Examiner finally rejecting Claims 1, 3-13, 17, 18, and 20.

The item(s) checked below are appropriate:

1. ☒ An extension of time to respond to the final rejection  
\_\_\_\_\_ was granted on \_\_\_\_\_.  
☒ is requested for 1 month.
2. \_\_\_\_\_ A timely response to the final rejection has been filed, as provided in  
841 O.G. 1411.
3. ☒ Fee \$500.00:  
\_\_\_\_\_ Not required (Fee paid in prior appeal)  
☒ The Commissioner of Patents is hereby authorized to charge any  
fees which may be required or credit any overpayment to the deposit  
account of Texas Instruments Incorporated, Account No. 20-0668.  
**This form is submitted in duplicate.**

12/27/2005 EAREGAY1 00000104 09862523

01 FC:1401 500.00 DA  
02 FC:1251 120.00 DA

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